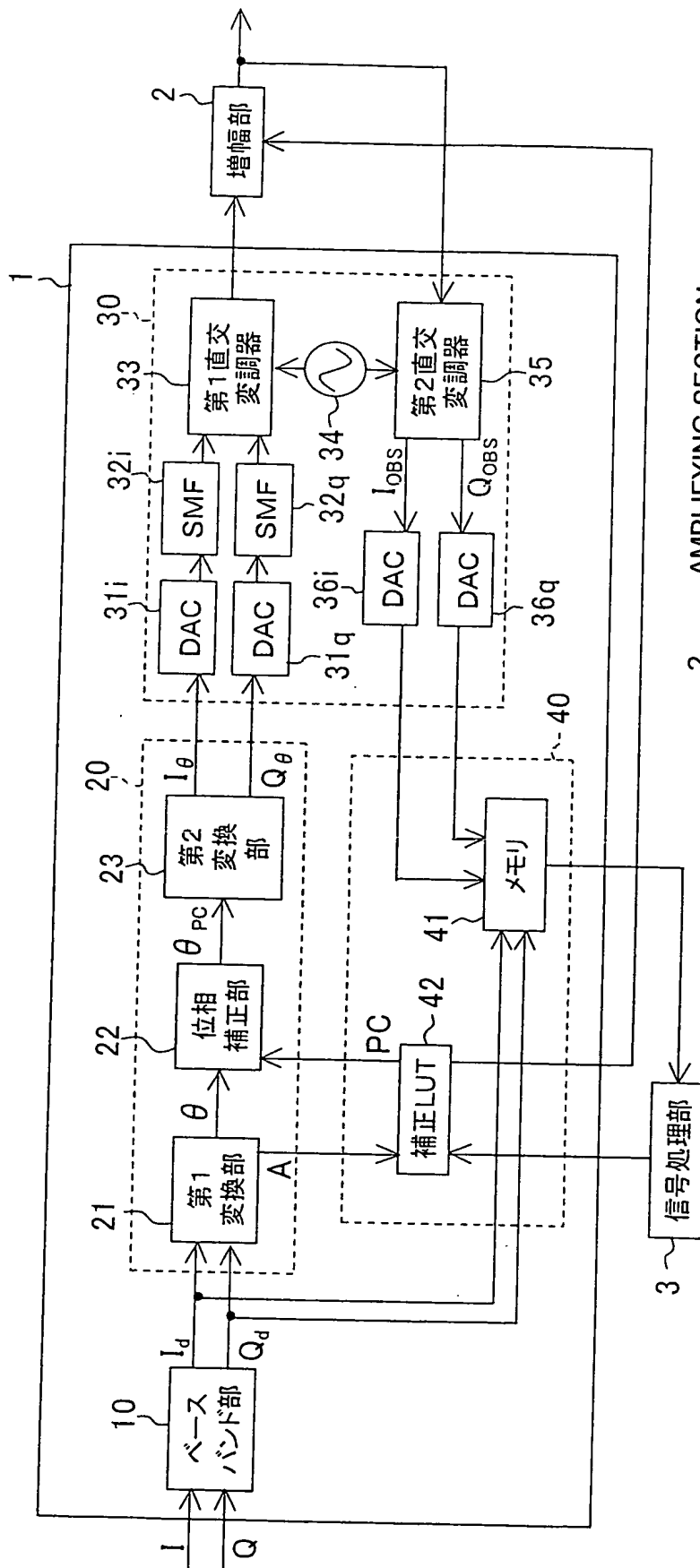


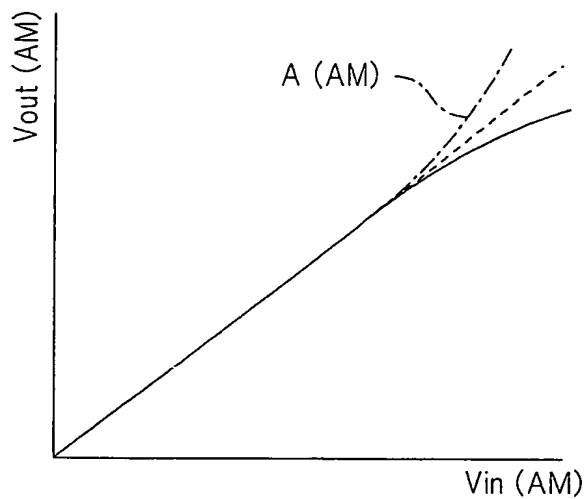
図1 Fig. 1



- 2 AMPLIFYING SECTION
- 3 SIGNAL PROCESSING SECTION
- 10 BASEBAND SECTION
- 21 FIRST CONVERTING SECTION
- 22 PHASE CORRECTING SECTION
- 23 SECOND CONVERTING SECTION
- 33 FIRST ORTHOGONAL MODULATOR
- 35 SECOND ORTHOGONAL MODULATOR
- 41 MEMORY
- 42 CORRECTION LUT

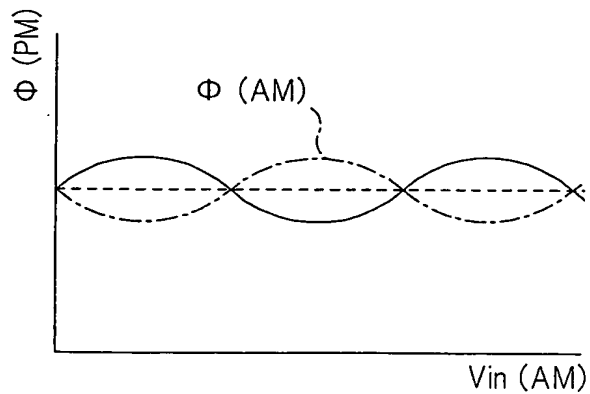
Fig. 2

~~Fig. 2~~

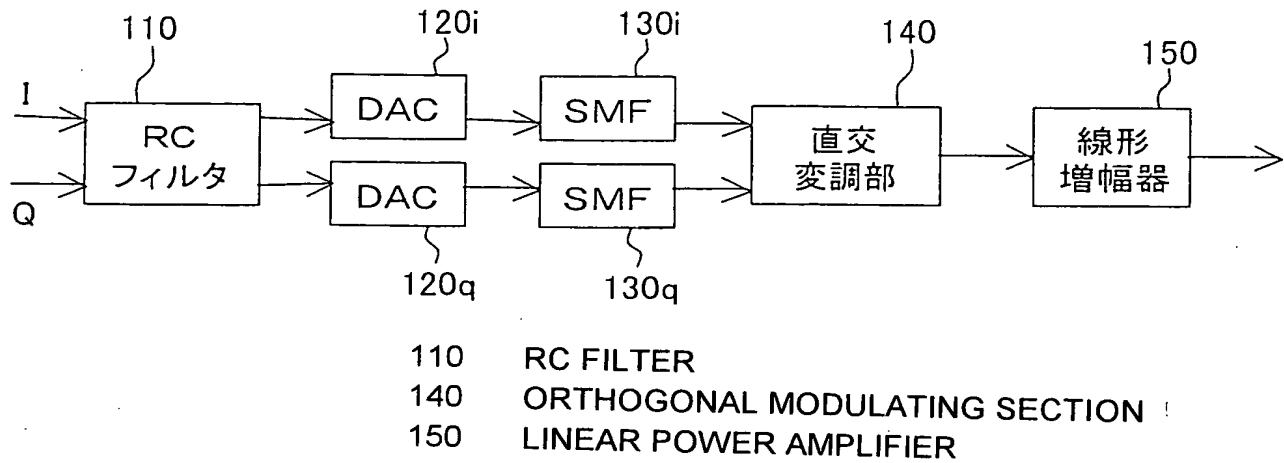


~~Fig. 3~~

Fig. 3



~~図4~~
 Fig. 4



~~図5~~
 Fig. 5

